ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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Title of Invention	METHOD FOR CREATING PATTERNS FOR PRODUCING INTEGRATED CIRCUITS

Application Number : 10/621114

Confirmation Number: 9085

First Named Applicant: Louis SCHEFFER

Attorney Docket Number: CA7035292001

Art Unit: 2825

Examiner: Naum B. Levin

Search string: (5567550 or 5580687 or 5821014 or 6571383 or 20040013952 or 20040044984).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	5567550	1996-10-22	Smayling			
	2	5580687	1996-12-03	Leedy			
	3	5821014	1998-10-13	Chen et al.			
	4	6571383	2003-05-27	Butt et al.			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
	1	20040013952	2004-01-22	Elian et al.			
	2	20040044984	2004-03-04	Keogan et al.			

Signature

Examiner Name	Date